Docket No.: 50090-270

**PATENT** 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

atsunobu HORI, et al.

Serial No.: 09/756,846

Filed: January 10, 2001

Examiner:

SEMICONDUCTOR DEVICE HAVING A LAYERED WIRING STRUCTURE

SUPPLEMENTAL INFORMATION DISCLOSURE

ssioner for Patent For:

Commissioner for Patents Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Supplemental Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of each non-English language reference, if any, is discussed in the present specification.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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**Date: April 12, 2001** Facsimile: (202) 756-8087

SHEET 1 OF 1

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INFORMATION DISCLOSURE					ATTY. DOCKET NO.		SERIAL NO.		
CITATION IN AN				50090-270		09/756,846			
APPLICATION									
on will				APPLICANT Katsunobu HORI, et al.					
(PTO-1449)				FILING DATE  January 10, 2001		GROUP <del>2814</del> 291			
U.S. PATENT DOCUMENTS									
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INITIALS	PATENT NO.	DATE	COUNTRY		CLASS	SUBCLASS	Yes	No	
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
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EXAMINER	EXAMINER DATE CONSIDERED 07/16/02								
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.